Title	Reduction in the short-circuit current density of silicon heterojunction photovoltaic modules subjected to potential-induced degradation tests
Author(s)	Yamaguchi, Seira; Yamamoto, Chizuko; Ohdaira, Keisuke; Masuda, Atsushi
Citation	Solar Energy Materials and Solar Cells, 161: 439- 443
Issue Date	2016-12-27
Туре	Journal Article
Text version	author
URL	http://hdl.handle.net/10119/16136
Rights	Copyright (C)2016, Elsevier. Licensed under the Creative Commons Attribution-NonCommercial-NoDerivatives 4.0 International license (CC BY-NC-ND 4.0).  [http://creativecommons.org/licenses/by-nc-nd/4.0/] NOTICE: This is the author's version of a work accepted for publication by Elsevier.  Changes resulting from the publishing process, including peer review, editing, corrections, structural formatting and other quality control mechanisms, may not be reflected in this document. Changes may have been made to this work since it was submitted for publication. A definitive version was subsequently published in Seira Yamaguchi, Chizuko Yamamoto, Keisuke Ohdaira, Atsushi Masuda, Solar Energy Materials and Solar Cells, 161, 2016, 439-443, http://dx.doi.org/10.1016/j.solmat.2016.12.027
Description	



# Reduction in the short-circuit current density of silicon heterojunction photovoltaic modules subjected to potential-induced degradation tests

Seira Yamaguchi<sup>a,\*</sup>, Chizuko Yamamoto<sup>b</sup>, Keisuke Ohdaira<sup>a</sup>, Atsushi Masuda<sup>b</sup>

<sup>a</sup>Graduate School of Advanced Science and Technology, Japan Advanced Institute of Science and Technology (JAIST), Nomi, Ishikawa 923-1292, Japan
 <sup>b</sup>Research Center for Photovoltaics, National Institute of Advanced Industrial Science and Technology (AIST), Tsukuba, Ibaraki 305-8568, Japan

#### **Abstract**

This letter deals with the potential-induced degradation (PID) of silicon heterojunction (SHJ) photovoltaic (PV) modules. After rapid indoor PID tests applying a voltage of -1000 V at 85 °C, the modules exhibited a significant reduction in short-circuit current density ( $J_{\rm sc}$ ). On the other hand, the dark current density–voltage characteristics of the modules were intact after the PID tests, indicating that the reduction in  $J_{\rm sc}$  is attributed not to carrier recombination but to optical loss. A degraded module slightly recovered its performance loss upon applying a positive bias but complete recovery was not observed, showing that the PID of SHJ PV modules is not reversible. A module with an ionomer encapsulant showed high PID resistance, revealing that the degradation of SHJ PV modules can be prevented by the use of ionomer encapsulants.

*Keywords:* Potential-induced degradation, Silicon heterojunction solar cell, Photovoltaic module, Optical loss, Ionomer encapsulant

## 1. Introduction

Silicon heterojunction (SHJ) photovoltaic (PV) cells have recently attracted attention owing to their high power-conversion efficiency [1–6]. Typical SHJ cells

Email addresses: s-yamaguchi@jaist.ac.jp (Seira Yamaguchi), ohdaira@jaist.ac.jp (Keisuke Ohdaira), atsushi-masuda@aist.go.jp (Atsushi Masuda)

<sup>\*</sup>Corresponding author

have an n-type crystalline silicon (c-Si) base and hydrogenated amorphous silicon (a-Si:H)-based passivation films on both sides, which leads to a high open-circuit voltage ( $V_{oc}$ ) and high efficiency [1–5]. A high  $V_{oc}$  of over 730 mV and efficiency of over 24% have been reported for both-side-contact structures [4, 5]. Masuko *et al.* have achieved an efficiency of 25.6% for an interdigitated back-contact (IBC) SHJ structure [6]. Furthermore, Kaneka Corporation has very recently achieved the highest efficiency of 26.3% for the IBC SHJ structure [7]. According to the International Technology Roadmap for Photovoltaic (seventh edition) [8], the market share of SHJ solar cells is expected to increase. With the growing market share, the reliability and long-term stability of SHJ PV modules will become increasingly important.

Potential-induced degradation (PID), which is associated with high electric potential differences between grounded frames and cells, has been identified as one of the most important reliability issues, particularly in large-scale PV systems [9–11]. To utilize SHJ PV cells, it is therefore important to understand the PID behavior of modules fabricated using the cells. There have been several studies on the PID of commercial SHJ PV modules [12–14], all of which reported that commercial SHJ PV modules exhibit high PID resistance under both negative and positive biases. These studies did not, however, demonstrate the higher PID resistance of the SHJ PV cells themselves because the encapsulation materials used in the studies were not disclosed, and their high PID resistance may have been due to cover glass and/or an encapsulant. In one of the studies [13], it was argued that SHJ solar cells do not undergo PID because they have no insulating layers and charge accumulation does not occur. This argument is probably based on several earlier studies on the mechanism of the PID of conventional p-type c-Si PV modules (for example Refs. [15, 16]). In these studies [15, 16], it was argued that the PID of p-type c-Si PV modules is associated with charge accumulation in the insulating passivation layer. However, researchers now consider the root cause of the PID of p-type c-Si PV modules to be the short-circuiting of the p-n junctions due to two-dimensional conductive sodium layers segregated in stacking faults (so-called sodium-decorated stacking faults) near the front c-Si-surface [17, 18]. To understand the actual effect of PID on SHJ PV cells, one should investigate SHJ PV modules with a standard encapsulation configuration involving conventional tempered cover glass and an ethylene-vinyl acetate copolymer (EVA) encapsulant.

In this work we demonstrate, through a rapid indoor PID test, that SHJ PV modules undergo PID characterized by a reduction in short-circuit current density  $(J_{sc})$ , which is probably due to the optical loss occurring in the front transparent

conductive oxide (TCO) layer and/or in the encapsulant. Moreover, we investigate whether the PID of SHJ PV modules can be prevented by the use of a high-electric-resistance encapsulant.

# 2. Experimental procedure

Single-cell SHJ PV modules with an area of  $180\times180~\text{mm}^2$  were fabricated using commercial bifacial SHJ solar cells with a rear-side emitter and indium-oxide-based TCO layers with an area of  $156\times156~\text{mm}^2$ . The module had a 3.2-mm-thick tempered white cover glass/450- $\mu$ m-thick EVA sheet (fast cure type)/SHJ solar cell (with the emitter side down)/450- $\mu$ m-thick EVA sheet (fast cure type)/typical white backsheet [38- $\mu$ m-thick poly(vinyl fluoride) sheet/250  $\mu$ m-thick poly(ethylene terephthalate) sheet/38- $\mu$ m-thick poly(vinyl fluoride) sheet] structure. The EVA sheets used in this study were also typical ones with a volume resistivity of 1.5 ×  $10^{14}~\Omega$ ·cm, and they were not capable of preventing PID [19–27]. Our module-lamination process consisted of two steps: a degassing step for 5 min and an adhesion step for 15 min, and both steps took place at 135 °C.

The PID tests were performed by applying a negative bias to connected module-interconnector ribbons with respect to an aluminum plate placed on the module cover glass in a heating chamber maintained at 85 °C. Herein, we use the terms "negative bias" and "positive bias" for biases that produce cells with negative and positive potentials with respect to the aluminum plate, respectively. We disregarded the effect of moisture ingress into the modules on the PID phenomena because the humidity in the heating chamber was very low (approximately 2% RH). To evaluate the degradation, we performed dark and one-sun-illuminated current density–voltage (J-V), external quantum efficiency (EQE), and electroluminescence (EL) measurements on the modules before and after the PID tests.

## 3. Results and discussion

It has been reported that applying a high negative bias to cells leads to the degradation of many types of PV modules, such as conventional p-type c-Si [9–11], n-type front-emitter c-Si [24, 25], n-type rear-emitter c-Si [26], n-type IBC c-Si [28], amorphous Si (a-Si) [12, 27], Cu(In,Ga)Se<sub>2</sub> (CIGS) [27, 29], and CdTe [12, 29] PV modules. Herein, we first clarify how the *J*–*V* characteristics change upon applying negative bias. Fig. 1 shows representative one-sun-illuminated *J*–*V* characteristics of the SHJ PV modules before and after the PID tests in which

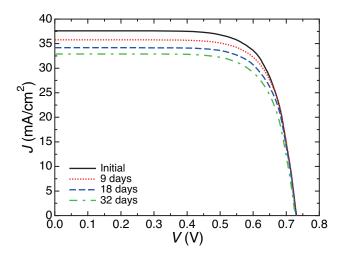


Figure 1: Representative one-sun-illuminated J-V characteristics of the SHJ PV modules before and after the PID tests applying a voltage of -1000 V.

a voltage of -1000 V was applied.  $J_{\text{sc}}$  gradually decreases with increasing PIDstress duration. The fill factor (FF) and  $V_{\rm oc}$  are, on the other hand, almost unchanged after the PID tests. These results indicate that SHJ PV modules undergo PID under negative bias and that the degradation is mainly characterized by a reduction in  $J_{sc}$ . The reduction in  $J_{sc}$  originates not from enhanced carrier recombination but from optical loss because their dark J-V characteristics are unchanged after the PID tests (not shown here). This can also be verified from Fig. 2, which shows the EQE characteristics of the unaffected and degraded SHJ PV modules. After the PID test for 13 days, the EQE is reduced in the entire wavelength range (see the dotted red line in Fig. 2). This suggests that the reduction in  $J_{\rm sc}$  originates from optical loss, because the EQE must be reduced only in a specific wavelength range if there is recombination loss in the cell. Also shown in Fig. 2 is a comparison of EQEs extracted from positions near the center and the edge of the cell. The reduction in EQE near the cell edge is greater than that near the center, indicating that the extent of optical loss depends on the position in the cell. The optical loss is probably due to the darkening of the TCO layer, which will be explained below. The position dependence of the reduction in EQE may be due to the nonuniformity of the properties of the TCO film, such as its composition.

Fig. 3 shows the dependence of  $J_{\rm sc}/J_{\rm sc,0}$  on the applied voltage during PID tests, where  $J_{\rm sc,0}$  is the initial  $J_{\rm sc}$ . The reduction rate of  $J_{\rm sc}$  seems to be affected by

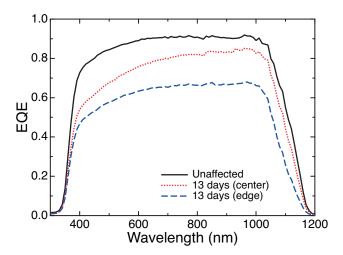


Figure 2: EQE characteristics of the unaffected SHJ PV module and those after the PID tests applying a voltage of -1000 V for 13 days. For the degraded module, EQEs were extracted from two positions near the center and the edge of the cell. The area of illumination in this EQE measurement was  $10 \times 10 \text{ mm}^2$ .

the voltage. The degradation is characterized only by a reduction in  $J_{\rm sc}$  even when the applied voltage is increased from  $-1000\,\mathrm{V}$  to  $-2000\,\mathrm{V}$ , which may suggest that the PID behavior is unchanged regardless of the magnitude of applied voltages. At present, it is unclear whether the PID of SHJ PV modules saturates for longer duration than that employed in this study.

The optical loss is probably caused by the darkening of the front TCO layer. As shown in Figs. 4a and 4b, the EL image of a module becomes darkened after the PID test with a voltage of -1000 V applied for 32 days. The loss in luminescence is not attributed to the enhancement of a non-radiative recombination process but indicates enhanced optical loss because the diode characteristic is unchanged after the PID test, as mentioned above. Figs. 5a and 5b show EL images of a module before and after the PID test, respectively, with a voltage of -2000 V applied for 21 days. Note that only the degradation is accelerated by the increase in the applied voltage and that the degradation behavior is the same as that under a bias of -1000 V. In Fig. 5b, a characteristic circle-shaped bright pattern can be seen in the middle of the cell. This indicates that the optical loss near the edge is greater than that near the center, which is consistent with the results of the EQE measurement. This pattern can also be seen by eye under light from a fluorescent lamp. The darkened TCO layer interrupts the incident light, lead-

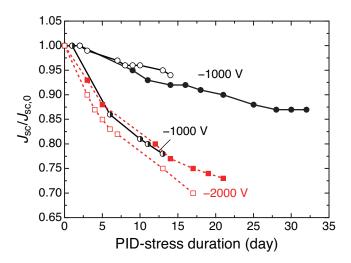


Figure 3: Dependence of the  $J_{\rm sc}/J_{\rm sc,0}$  reduction behavior of SHJ PV modules on applied voltage during PID tests.

ing to a significant reduction in  $J_{sc}$ . The darkening might be due to precipitates formed by the chemical reduction of the TCO. It is well known that indium-oxidebased TCO films are relatively easily reduced by exposure to a strongly reducing environment, and metal precipitates can be formed by the chemical reduction of oxidized species in such an environment [30–36]. In our PID tests, the PV cells correspond to the cathode in the oxidation-reduction reaction. Some reactions involoving chemical reduction should occur on the cell surface. One candidate is the chemical reduction of sodium (Na) ions, which may originate from the cover glass [11] and/or contaminants on the cell surface [37]:  $Na^+ + e^- \rightarrow Na$ . The Na atoms and/or other reductants that are produced in oxidation of the Na atoms might induce the formation of metal precipitates. In superstrate a-Si thin-film PV modules, the delamination of fluorine-doped tin oxide layers has been reported to be induced by a high negative bias and moisture ingress [38]. According to a model proposed by Jansen et al. [39], this delamination involves the reduction of the TCO induced by elemental hydrogen produced in chemical reaction between reduced Na and moisture. In our experiment, the effect of moisture ingress was able to be neglected because the relative humidity was very low. Moisture originally contained in the EVA encapsulant may affect the chemical reduction of the TCO layer, or chemical reduction unrelated to moisture may occur. Regarding the PID of CIGS PV modules, darkening of the front zinc-oxide (ZnO)-based TCO

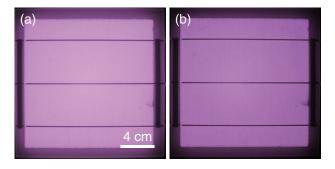


Figure 4: EL images of SHJ PV modules (a) before and (b) after the PID tests with -1000 V applied for 32 days.

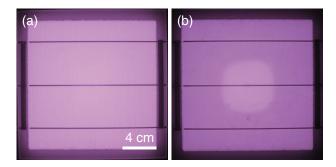


Figure 5: EL images of SHJ PV modules (a) before and (b) after the PID tests with -2000 V applied for 21 days.

layers does not occur during PID tests despite an increased Na content in the ZnO layers [27]. This is likely due to the fact that ZnO-based TCO films are resistant to chemical reduction compared with indium oxide-based TCO [36]. Much additional work is, however, required to understand the darkening of the TCO.

At present, we cannot deny the possibility of the darkening of the encapsulant material. We have not yet analyzed the optical properties of the EVA encapsulant of PID-affected PV modules. However, it should be noted that in our previous studies, EVA encapsulants have not been confirmed to be darkened by high voltages.

It has been reported that the PID of many types of PV modules, such as conventional p-type c-Si [9], n-type front-emitter c-Si [24], and CIGS [27] modules, can be recovered by applying a positive bias. To investigate whether the PID of SHJ PV modules is reversible, we applied a positive bias of +2000 V to mod-

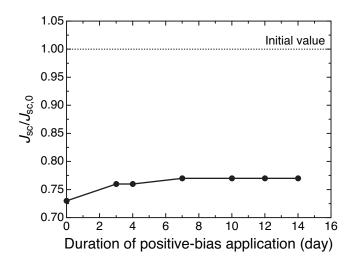


Figure 6: Dependence of  $J_{\rm sc}/J_{\rm sc,0}$  of a degraded SHJ PV module on the duration of the recovery test with a positive bias of +2000 V. The module was degraded in a PID test applying a voltage of -2000 V for 21 days prior to the recovery test.

ules after the PID test in which a voltage of -2000 V was applied for 21 days at 85 °C. Shown in Fig. 6 is the dependence of  $J_{\rm sc}/J_{\rm sc,0}$  of the SHJ PV module on the duration of bias application. Note that  $V_{\rm oc}$  and FF are almost unchanged after the PID and recovery tests. In the initial stage,  $J_{\rm sc}$  can be seen to slightly recover. However,  $J_{\rm sc}$  then reaches a constant value. These observations indicate that the PID of SHJ PV modules is almost irreversible, unlike the PID of many types of modules. This irreversibility might be due to that of the darkening of the TCO, which is limited by an irreversible chemical reaction. The mechanism of the slight recovery of  $J_{\rm sc}$  shown in Fig. 6 is not clear but might be related to the back-diffusion of Na<sup>+</sup> or the oxidation of metal precipitates.

From the viwepoint of installation in large-scale PV systems, it is important to compare the PID resistance of the SHJ PV modules with that of other types of modules. We previously reported the PID test results of p-type c-Si [27], a-Si thin-film [27], CIGS thin-film [27], n-type front-emitter c-Si [24], and n-type rearemitter c-Si [26] PV modules. Note that all the PV modules except for the a-Si thin-film module have the same encapsulant and cover glass. (The a-Si thin-film module has a superstrate configuration; thus, there is no encapsulant between the front glass and the cells.) After the tests for 1 day under the same temperature and voltage conditions as those in this study,  $P_{\text{max}}/P_{\text{max},0}$  for the a-Si [27], CIGS [27],

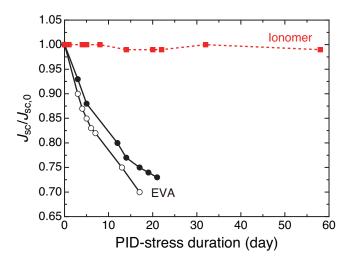


Figure 7: Dependence of  $J_{\rm sc}/J_{\rm sc,0}$  of the SHJ PV modules with the EVA encapsulant or the ionomer encapsulant on PID-stress duration. The applied voltage was set to -2000 V in this experiment.

and n-type rear-emitter c-Si [26] PV modules decreases to 0.23, 0.95, and 0.93, respectively, where  $P_{\text{max}}$  is the maximum output power and  $P_{\text{max},0}$  is the initial  $P_{\text{max}}$ . After the same tests for 2 h,  $P_{\text{max}}/P_{\text{max},0}$  for the p-type c-Si [27] and n-type front-emitter c-Si [24] PV modules decreases to 0.04–0.4 and 0.85. In contrast,  $P_{\text{max}}/P_{\text{max},0}$  for the SHJ PV modules is almost unchanged after the PID test for 2 days. These results indicate that the SHJ PV modules have a high PID resistance. This may be an advantage of the SHJ PV modules. Moreover, we can further improve the reliability and long-term stability of SHJ PV modules, as mentioned below. Note that the PID of p-type c-Si [9], n-type front-emitter c-Si [24], and CIGS thin-film [27] PV modules can be recovered by applying a positive bias whereas the PID of SHJ PV modules cannot be recovered by such a simple way.

To determine a possible measure to prevent PID, we performed PID tests on an SHJ PV module with an ionomer encapsulant with a volume resistivity of  $10^{16}$   $\Omega$ ·cm. Ionomer encapsulants have been reported to effectively prevent the PID of several types of modules, such as conventional p-type c-Si [40], n-type front-emitter c-Si [24], and CIGS thin-film [27] PV modules. Note that the PID conditions of -2000 V and 85 °C used in this experiment are severe. Fig. 7 shows the dependence of  $J_{\rm sc}/J_{\rm sc,0}$  for the SHJ PV modules with the EVA and with the ionomer on the PID-stress duration. Note that  $V_{\rm oc}$  and FF for both modules are almost the same before and after the PID tests.  $J_{\rm sc}/J_{\rm sc,0}$  for the EVA module de-

creases to 0.73 after the PID test for 21 days. In contrast, the ionomer module maintains the initial value of  $J_{\rm sc}/J_{\rm sc,0}$  even after the PID tests for 58 days. These results indicate that the use of ionomer encapsulants can effectively prevent the PID of SHJ PV modules. This is likely due to a strongly reduced leakage current caused by the noteworthy high electric resistance of the ionomer encapsulant.

#### 4. Conclusions

In this study, we have investigated the PID of SHJ PV modules by performing a rapid indoor PID test. We have demonstrated that SHJ PV modules undergo PID characterized by a reduction in  $J_{\rm sc}$  due to optical loss that probably occurs in the front TCO layer. This optical loss may be due to metal precipitates formed by the chemical reduction of oxidized species. We have also shown that SHJ PV modules have high PID resistance compared with conventional p-type c-Si, n-type front-emitter c-Si, n-type rear-emitter c-Si, a-Si thin-film, and CIGS thin-film PV modules. This high reliability can be further improved by the use of ionomer encapsulants.

# Acknowledgements

This work was supported by the New Energy and Industrial Technology Development Organization. The authors thank Dr. Kohjiro Hara of AIST for performing the EQE measurements and Sachiko Jonai of AIST for providing information about the volume resistivity of the EVA and ionomer sheets. The authors also thank Du Pont–Mitsui Polychemicals Co., Ltd. for providing the ionomer encapsulants.

#### References

- [1] S. De Wolf, A. Descoeudres, Z. C. Holman, C. Ballif, High-efficiency silicon heterojunction solar cells: A review, Green 2 (2012) 7–24.
- [2] T. Mishima, M. Taguchi, H. Sakata, E. Maruyama, Development status of high-efficiency HIT solar cells, Sol. Energy Mater. Sol. Cells 95 (2011) 18– 21.
- [3] T. Kinoshita, D. Fujishima, A. Yano, A. Ogane, S. Tohoda, K. Matsuyama, Y. Nakamura, N. Tokuoka, H. Kanno, H. Sakata, M. Taguchi, E. Maruyama, The approaches for high efficiency HIT solar cell with very thin ( $<100 \ \mu m$ )

- silicon wafer over 23%, in: Proceedings of the 26th European Photovoltaic Solar Energy Conference and Exhibition, 5–9 September, Hamburg, Germany, 2011, pp. 871–874.
- [4] M. Taguchi, A. Yano, S. Tohoda, K. Matsuyama, Y. Nakamura, T. Nishiwaki, K. Fujita, E. Maruyama, 24.7% record efficiency HIT solar cell on thin silicon wafer, IEEE J. Photovoltaics 4 (2014) 96–99.
- [5] D. Adachi, J. L. Hernández, K. Yamamoto, Impact of carrier recombination on fill factor for large area heterojunction crystalline silicon solar cell with 25.1% efficiency, Appl. Phys. Lett. 107 (2015) 233506.
- [6] K. Masuko, M. Shigematsu, T. Hashiguchi, D. Fujishima, M. Kai, N. Yoshimura, T. Yamaguchi, Y. Ichihashi, T. Mishima, N. Matsubara, T. Yamanishi, T. Takahama, M. Taguchi, E. Maruyama, S. Okamoto, Achievement of more than 25% conversion efficiency with crystalline silicon heterojunction solar cell, IEEE J. Photovoltaics 4 (2014) 1433–1435.
- [7] Kaneka Corporation, World's highest conversion efficiency of 26.33% achieved in a crystalline silicon solar cell —A world first in a practical cell size—, http://www.kaneka.co.jp/kanekae/images/topics/1473811995/1473811995\_101.pdf, 2016 (accessed October 12, 2016).
- [8] SEMI PV Group Europe, International Technology Roadmap for Photovoltaic (ITRPV). http://www.itrpv.net/, 2016 (accessed May 23, 2016).
- [9] S. Pingel, O. Frank, M. Winkler, S. Daryan, T. Geipel, H, Hoehne, J. Berghold, Potential induced degradation of solar cells and panels, in: Proceedings of 35th IEEE Photovoltaic Specialists Conference, 20–25 June, Honolulu, HI, USA, 2010, pp. 2817–2822.
- [10] J. Berghold, O. Frank, H. Hoehne, S. Pingel, B. Richardson, M. Winkler, Potential induced degradation of solar cells and panels, in: Proceedings of the 25th European Photovoltaic Solar Energy Conference and Exhibition/5th World Conference on Photovoltaic Energy Conversion, 6–10 September, Valencia, Spain, 2010, pp. 3753–3759.
- [11] P. Hacke, M. Kempe, K. Terwilliger, S. Glick, N. Call, S. Johnston, S. Kurtz, I. Bennett, M. Kloos, Characterization of multicrystalline silicon modules

- with system bias voltage applied in damp heat, in: Proceedings of the 25th European Photovoltaic Solar Energy Conference and Exhibition/5th World Conference on Photovoltaic Energy Conversion, 6–10 September, Valencia, Spain, 2010, pp. 3760–3765.
- [12] Z. Xiong, T. M. Walsh, A. G. Aberle, PV module durability testing under high voltage biased damp heat conditions, Energy Procedia 8 (2011) 384– 389.
- [13] T. Ishiguro, H. Kanno, M. Taguchi, S. Okamoto, Study on PID resistance of HIT PV modules, presented at 2013 Photovoltaic Module Reliability Workshop, 26–27 February, Golden, CO, USA, 2013.
- [14] N. Riedel, L. Pratt, E. Moss, M. Yamasaki, 600 hour potential induced degradation (PID) testing on silicon, CIGS, and HIT modules, presented at 2015 Photovoltaic Reliability Workshop, 24–27 February, Golden, CO, USA, 2015.
- [15] V. Naumann, C. Hagendorf, S. Grosser, M. Werner, J. Bagdahn, Micro structural root cause analysis of potential induced degradation in c-Si solar cells, Energy Procedia 27 (2012) 1–6.
- [16] J. Bauer, V. Naumann, S. Großer, C. Hagendorf, M. Schütze, O. Breitenstein, On the mechanism of potential-induced degradation in crystalline silicon solar cells, Phys. Status Solidi: Rapid Res. Lett. 6 (2012) 331–333.
- [17] V. Naumann, D. Lausch, A. Graff, M. Werner, S. Swatek, J. Bauer, A. Hähnel, O. Breitenstein, S. Großer, J. Bagdahn, C. Hagendorf, The role of stacking faults for the formation of shunts during potential-induced degradation of crystalline Si solar cells, Phys. Status Solidi: Rapid Res. Lett. 7 (2013) 315–318.
- [18] V. Naumann, D. Lausch, A. Hähnel, J. Bauer, O. Breitenstein, A. Graff, M. Werner, S. Swatek, S. Großer, J. Bagdahn, C. Hagendorf, Explanation of potential-induced degradation of the shunting type by Na decoration of stacking faults in Si solar cells, Sol. Energy Mater. Sol. Cells 120 (2014) 383–389.
- [19] K. Hara, H. Ichinose, T. N. Murakami, A. Masuda, Crystalline Si photovoltaic modules based on TiO<sub>2</sub>-coated cover glass against potential-induced degradation, RSC Adv. 4 (2014) 44291–44295.

- [20] K. Hara, S. Jonai, A. Masuda, Crystalline Si photovoltaic modules functionalized by a thin polyethylene film against potential and damp-heat-induced degradation, RSC Adv. 5 (2015) 15017–15023.
- [21] S. Jonai, K. Hara, Y. Tsutsui, H. Nakahama, A. Masuda, Relationship between cross-linking conditions of ethylene vinyl acetate and potential induced degradation for crystalline silicon photovoltaic modules, Jpn. J. Appl. Phys. 54 (2016) 08KG01.
- [22] A. Masuda, Y. Hara, S. Jonai, Consideration on Na diffusion and recovery phenomena in potential-induced degradation for crystalline Si photovoltaic modules, Jpn. J. Appl. Phys. 55 (2016) 02BF10.
- [23] S. Yamaguchi, A. Masuda, K. Ohdaira, Behavior of the potential-induced degradation of photovoltaic modules fabricated using flat mono-crystalline silicon cells with different surface orientations, Jpn. J. Appl. Phys. 55 (2016) 04ES14.
- [24] K. Hara, S. Jonai, A. Masuda, Potential-induced degradation in photovoltaic modules based on n-type single crystalline Si solar cells, Sol. Energy Mater. Sol. Cells 140 (2015) 361–365.
- [25] S. Yamaguchi, A. Masuda, K. Ohdaira, Progression of rapid potential-induced degradation of n-type single-crystalline silicon photovoltaic modules, Appl. Phys. Express (in press).
- [26] S. Yamaguchi, A. Masuda, K. Ohdaira, Changes in current density–voltage and external quantum efficiency characteristics of n-type single-crystalline silicon photovoltaic modules with a rear-side emitter undergoing potential-induced degradation, Sol. Energy Mater. Sol. Cells 151 (2016) 113–119.
- [27] S. Yamaguchi, S. Jonai, K. Hara, H. Komaki, Y. Shimizu-Kamikawa, H. Shibata, S. Niki, Y. Kawakami, A. Masuda, Potential-induced degradation of Cu(In,Ga)Se<sub>2</sub> photovoltaic modules, Jpn. J. Appl. Phys. 54 (2015) 08KC13.
- [28] V. Naumann, T. Geppert, S. Großer, D. Wichmann, H.-J. Krokoszinski, M. Werner, C. Hagendorf, Potential-induced degradation at interdigitated back contact solar cells, Energy Procedia 55 (2014) 498–503.
- [29] P. Hacke, K. Terwilliger. S. H. Glick, G. Perrin, J. Wohlgemuth, S. Kurtz, K. Showalter, J. Sherwin, E. Schneller, S. Barkaszi, R. Smithc, Survey

- of potential-induced degradation in thin-film modules, J. Photon. Energy 5 (2015) 053083.
- [30] J. C. C. Fan, J. B. Goodenough, X-ray photoemission spectroscopy studies of Sn-doped indium-oxide films, J. Appl. Phys. 48 (1977) 3524–3531.
- [31] M. Kitagawa, K. Mori, S. Ishihara, M. Ohno, T. Hirao, Y. Yoshioka, S. Kohiki, Interaction of hydrogenated amorphous silicon films with transparent conductive films, J. Appl. Phys. 54 (1983) 3269–3271.
- [32] S. Major, S. Kumar, M. Bhatnagar, K. L. Chopra, Effect of hydrogen plasma treatment on transparent conducting oxides, Appl. Phys. Lett. 49 (1986) 394–396.
- [33] R. Banerjee, S. Ray, N. Basu, A. K. Batabyal, A. K. Barua, Degradation of tin-doped indium-oxide film in hydrogen and argon plasma, J. Appl. Phys. 62 (1987) 912–916.
- [34] R. Oesterlein, H.-J. Krokoszinski, Optical dispersion in electron-beamevaporated indium tin oxide thin films, Thin Solid Films 175 (1989) 241– 247.
- [35] T. Minami, H. Sato, H. Nanto, S. Takata, Heat treatment in hydrogen gas and plasma for transparent conducting oxide films such as ZnO, SnO<sub>2</sub> and indium tin oxide, Thin Solid Films 176 (1989) 277–282.
- [36] B. Drevillon, S. Kumar, P. R. i Cabarrocas, J. M. Siefert, *In situ* investigation of the optoelectronic properties of transparent conducting oxide/amorphous silicon interface, Appl. Phys. Lett. 54 (1989) 2088–2090.
- [37] V. Naumann, D. Lausch, C. Hagendorf, Sodium decoration of PID-s crystal defects after corona induced degradation of bare silicon solar cells, Energy Procedia 77 (2015) 397–401.
- [38] C. R. Osterwald, T. J. McMahon, J. A. del Cueto, Electrochemical corrosion of SnO<sub>2</sub>:F transparent conducting layers in thin-film photovoltaic modules, Sol. Energy Mater. Sol. Cells 79 (2003) 21–33.
- [39] K. W. Jansen, A. E. Delahoy, A laboratory technique for the evaluation of electrochemical transparent conductive oxide delamination from glass substrates, Thin Solid Films 423 (2003) 153–160.

[40] J. Kapur, K. M. Stika, C. S. Westphal, J. L. Norwood, B. Hamzavytehrany, Prevention of potential-induced degradation with thin ionomer film, IEEE J. Photovoltaics 5 (2015) 219–223.