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Description	

## **Vacuum deposition of CsPbI<sub>3</sub> layers on textured Si for perovskite/Si tandem solar cells**

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We attempt the conformal deposition of CsPbI<sub>3</sub> layers on pyramidal-shaped textured crystalline Si (c-Si) surfaces, aiming at application to Perovskite/Si tandem solar cells. CsPbI<sub>3</sub> layers are deposited through vacuum evaporation on textured c-Si surfaces with various pyramid sizes. Conformal CsPbI<sub>3</sub> layers are formed on the texture c-Si, which is, in principle, difficult to be realized by conventional solution process. We also confirmed a reduction in the optical reflectance of CsPbI<sub>3</sub>/Si structures by ~10% (absolute) by using textured c-Si compared to the case of flat Si. A co-evaporation method can prevent the aggregation of CsI particles, which are seen when the films are formed by sequential evaporation. These results indicate the feasibility of Perovskite/c-Si tandem cells with textured c-Si, leading to low optical reflectivity and high photocurrent.

## 1. Introduction

Perovskite solar cells such as organic-inorganic Perovskite  $\text{CH}_3\text{NH}_3\text{PbI}_3$  have been attracting attention as high-efficiency thin-film solar cells, because they show a dramatic improvement of power conversion efficiency (PCE) from 3.8<sup>1)</sup> to 23.3%<sup>2)</sup> in only 9 years from the first report in 2009. Perovskite/Si tandem solar cells are one of the solutions to overcome the PCE of crystalline silicon (c-Si) solar cells, which have been widely used and dominated the present share of photovoltaic market. The currently reported maximum PCE of the c-Si cells is 26.7%<sup>3)</sup>, closely approaching the Shockley-Queisser (S-Q) limit<sup>4)</sup> of 29.4%<sup>5)</sup>. Perovskite/Si tandem solar cells can improve the S-Q limit up to 40.6%<sup>6)</sup>. Several Perovskite/Si tandem solar cells have been recently reported, and the present maximum PCEs of 4-terminal and 2-terminal (monolithic) Perovskite/Si tandem solar cells are 27.1%<sup>7)</sup> and 27.3%<sup>8)</sup>, respectively. To reach the PCE more than the theoretical limit of c-Si, it is necessary to optimize the fabrication process of the Perovskite solar cells on c-Si.

Perovskite solar cells used for the top cells of tandem solar cells are generally produced by solution processes such as spin coating.<sup>9)</sup> However, the solution processes require the wettability and smoothness of the underlying surface of substrates. c-Si solar cells generally have micrometer-order pyramidal-shaped texture structures on both sides of c-Si wafers for effective light trapping.<sup>10,11)</sup> The surface textures on c-Si will contribute to a reduction in the optical reflectance also in the case of Perovskite/Si tandem solar cells, and the Perovskite layers are required to be conformally formed on the pyramids. The unevenness on the textured c-Si surface of several  $\mu\text{m}$ , however, causes solution pooling in the valleys between the pyramids. This makes it difficult to form thin films that conformally cover the surfaces of c-Si pyramids. Only flat Si wafers have therefore been used in the solution process, and the PCE of the tandem cells is limited by the reflection loss due to the insufficient antireflection.<sup>12-14)</sup> If the Perovskite films are formed by vacuum deposition, the problem of the pooling of Perovskite materials will be solved. Although there have been several reports for the conformal formation of Perovskite films on textured Si, they partially includes solution process.<sup>15,16)</sup> The formation of Perovskite layers only through the vacuum process may be preferable in the future mass-production of the Perovskite/Si tandem cells.

In this study, vacuum evaporation has been applied for a new method to deposit  $\text{CsPbI}_3$  Perovskite layers on textured Si, instead of the conventional solution process. We have so

far reported the stable formation of fully inorganic Perovskite CsPbI<sub>3</sub> films without gasification by vacuum evaporation and the application of the CsPbI<sub>3</sub> films to a solar cell absorber.<sup>17, 18)</sup> CsPbI<sub>3</sub> has also been reported as having highly efficient optical and electrical properties.<sup>19–27)</sup> The currently reported maximum PCE of the CsPbI<sub>3</sub> cells is 13.5%.<sup>28)</sup> The vacuum deposition of thin films can realize, in general, high step coverage and can control the thickness of films precisely. We expect that these properties can be utilized for the conformal formation of Perovskite films on micrometer-sized Si pyramids. We have thus prepared Si wafers with texture structures with different sizes, and evaporated CsPbI<sub>3</sub> films thereon to verify the formation of continuous films with good uniformity and coverage. Moreover, to verify the antireflection effect by the usage of textured c-Si wafers, we compared optical reflectance spectra after the formation of the CsPbI<sub>3</sub> films on flat and textured c-Si and estimated an increase in photocurrent, as partly reported in our previous work.<sup>29)</sup> We have also compared with sequential and co-evaporation to determine a better method for the conformal deposition of CsPbI<sub>3</sub> films.

## 2. Experimental methods

We used 290- $\mu\text{m}$ -thick mirror-polished floating-zone-grown n-type Si(100) wafers with a resistivity of 1–5  $\Omega\text{cm}$ . After cleaning the wafers in ozone water for 5 minutes, alkali anisotropic etching was performed using two types of etching solutions with different additives (SE-2000H, SUN-X 600) to form pyramidal-shaped textures. The c-Si wafers were immersed in the etching solutions under the conditions shown in Table I. The textured wafers were then ultrasonically cleaned in deionized water for 15 minutes. We observed the surfaces of the textured Si wafers by scanning electron microscopy (SEM) and evaluated the size of pyramids.

Figure 1 shows the surface SEM images of textured Si wafers after alkali etching. Table I shows the heights of the pyramidal-shaped textures measured from Fig. 1. Textures with different sizes were obtained depending on etching solutions and conditions, which are indicated as [S], [M], and [L]. The surface image of [S] shows flat gaps, probably with (100) orientation, on the surface between the pyramids. On the contrary, there are no such gaps on the images of [M] and [L]. [L] has a larger average size of pyramids than [M].

Figure 2 shows the reflectance spectra of the textured and flat Si wafers. Table II shows

average reflectance ( $R_{AVE}$ ) calculated from Fig. 2 spectra in a wavelength range of 400 to 800 nm. The optical reflectance was reduced, compared to flat Si, by the formation of pyramidal-shaped textures formed under any conditions. [M] shows 15% (absolute) lower reflectance than a flat wafer. The textured surface reduces the optical reflection by the multiple reflection effect of reflected light being incident again on adjacent pyramids with Si(111) facets. [S] shows worse antireflection ability, which is probably due to remaining Si(100) in the gap. [L] shows higher reflectance than [M], which may be due to less uniform pyramid sizes.

We next formed CsPbI<sub>3</sub> layers on the textured Si wafers through two types of evaporation processes: sequential evaporation and co-evaporation. In the sequential evaporation, 150-nm-thick PbI<sub>2</sub> and 80-nm-thick CsI were evaporated in this order on the textured Si substrates at a pressure of  $2.0 \times 10^{-4}$  Pa. Small amounts of PbI<sub>2</sub> and CsI powders were placed in a K-cell with a diameter of 5 cm and a depth of 1.2 cm and a tungsten boat, respectively, inside the chamber and carefully heated to allow outgassing. The K-cell was heated at 420 °C, resulting in a growth rate of 0.5 Å/s for PbI<sub>2</sub>. The tungsten boat was electrically heated so that the growth rate of CsI was 0.3 Å/s. The thickness and growth rate were monitored with a quartz crystal oscillator. The K-cell and the tungsten boat were 30 and 35 cm from the substrates, respectively. In the co-evaporation, PbI<sub>2</sub> and CsI were evaporated simultaneously on the textured Si substrates at deposition rates of 0.5 and 0.3 Å/s for 50 min. These samples were then annealed at 350 °C for 1 minute on a hot plate to form CsPbI<sub>3</sub> layers. We prepared CsPbI<sub>3</sub> layers also on flat c-Si wafers in the same ways. The coverage of CsPbI<sub>3</sub> layers on flat and textured c-Si wafers was confirmed from the cross-sectional scanning electron microscope (SEM) images. The element distribution in the films was analyzed by energy dispersive X-ray spectroscopy (EDX).

PbI<sub>2</sub> films were formed on the texture Si also by a solution method for comparison to confirm that the conformal film formation by solution process is actually difficult. Textured Si wafers first received oxygen plasma treatment for 20 min. PbI<sub>2</sub> precursor solutions (230 mg/ml in N, N-dimethylformamide) stirred for >12 hours were then spin-coated on the textured Si wafers at 2000 rpm for 60 sec, followed by annealing at 100 °C for 10 min.

We measured the reflectance spectra of the CsPbI<sub>3</sub>/c-Si stacks using an ultraviolet-visible spectrophotometer (UV-vis) to compare reflection losses of the samples with flat and

textured Si wafers on which CsPbI<sub>3</sub> films were formed. Based on these results, we estimated an increase in photocurrent under the assumption that all the absorbed photons generate carriers and all the carriers are collected as a current. We evaluated, from the reflectance ( $R$ ) data, how much short-circuit current density ( $J_{SC}$ ) increases in Perovskite/Si tandem solar cells by the utilization of textured c-Si using the following eq. (1).

$$J_{SC} = q \int_{300}^{1200} N_{Photon(AM1.5G)} \cdot (1 - R) d\lambda \quad (1)$$

### 3. Results and discussion

#### 3.1 Sequential evaporation of CsPbI<sub>3</sub>

Figure 3 shows the cross-sectional SEM image of a PbI<sub>2</sub> film formed on textured Si by the solution process described in the previous section. Despite the improved wettability of the surface by the oxygen plasma treatment, spin-coated PbI<sub>2</sub> is aggregated and crystallized in the valleys between pyramids in the form of a liquid pool, while the top of the pyramids are covered with a PbI<sub>2</sub> film. This clearly indicates that the difficulty of conformally forming a perovskite film on micrometer-sized pyramids by a solution method.

We next carried out the vacuum evaporation of CsPbI<sub>3</sub> layers on the textured Si wafers with three types of pyramids: [S], [M] and [L]. Figure 4 shows the cross-sectional SEM images of CsPbI<sub>3</sub> layers deposited through sequential evaporation on the three types of textured wafers as well as on a flat Si wafer. The conformal formation of CsPbI<sub>3</sub> layers can be seen for all the pyramidal-shaped c-Si surfaces, regardless of the size of pyramids. These results indicate that it is possible to form CsPbI<sub>3</sub> layers with good uniformity and coverage on textured Si wafers by sequential evaporation, unlike in the case of conventional solution process. One can see protrusions in the CsPbI<sub>3</sub> films formed on textured c-Si particularly at the tips of the pyramids. Similar structures are seen also in the film formed on a flat c-Si wafer. These may originate from aggregated excess Cs. We confirmed the formation of an impurity crystals of Cs<sub>4</sub>PbI<sub>6</sub> by X-ray diffraction (XRD) in our previous studies<sup>17,18</sup>.

Figure 5 shows the optical reflectance spectra of three types of textured Si and flat Si coated with CsPbI<sub>3</sub> layers. A broad peak at a wavelength of ~800 nm due to optical interference is seen only in the spectrum for the flat Si sample. Average reflectance ( $R_{AVE}$ ) in a wavelength region of 400–800 nm are summarized in Table III. All the CsPbI<sub>3</sub> films

formed on textured Si showed  $R_{AVE}$  of  $\sim 10\%$  (absolute) lower than that of CsPbI<sub>3</sub> films on the flat wafer. This suggests that reflection loss on the CsPbI<sub>3</sub> surface can be greatly reduced by using textured Si. Note that  $R_{AVE}$  for the samples with textured Si wafers is almost independent of the size of pyramids. This means that low optical reflectance can be obtained on textured Si surfaces with any sizes.

To estimate maximum  $J_{SC}$  gain by the usage of textured Si, we assumed that all the unreflective photons are absorbed and produce photocurrent with 100% internal quantum efficiency, and the two sub-cell, CsPbI<sub>3</sub> and c-Si, produce the same current.  $J_{SC}$  for the flat and [M] size textured wafers were estimated to be 19.3 and 21.5 mA/cm<sup>2</sup>, respectively. This means that  $J_{SC}$  can be improved by  $\sim 2$  mA/cm<sup>2</sup> by using textured Si, which is a great benefit for the Perovskite/Si tandem cells.

### 3.2 Comparison of sequential and co-evaporations

Figure 6 shows the cross-sectional SEM images of sequentially evaporated CsI/PbI<sub>2</sub>/Si stacks before annealing for crystallization. We see two kinds of layers on the textured Si: (a) thin layers formed conformally on the pyramids, which appear gray in the image, and (b) columnar structures, which appear white, standing perpendicularly on the conformal layer. According to the order of sequential evaporation, these are identified to be PbI<sub>2</sub> and CsI, respectively. The formation of columnar CsI might be due to poor sticking probability and/or insufficient migration on PbI<sub>2</sub>.<sup>30)</sup> Once CsI columns are formed, they will grow selectively due to the self-projection effect, which may occur especially at protrude parts—the top of the pyramids.

Figure 7 shows the cross-sectional SEM and EDX mapping images of sequentially evaporated CsI/PbI<sub>2</sub>/Si stacks before annealing and CsPbI<sub>3</sub>/Si stacks after annealing. Cs is intensely detected particularly at the tops of pyramids before annealing, probably originating from columnar CsI. We still see strong Cs signals at the top of the pyramids even after annealing. This indicates that the aggregated Cs before annealing remains and the Cs-rich impurity phase of Cs<sub>4</sub>PbI<sub>6</sub> may be formed. In addition, Cs accumulation is partly confirmed also on Si(111) facets. From these results, we conclude that CsPbI<sub>3</sub> layers formed by sequential evaporation tend to contain a large amount of the impurity phase of Cs<sub>4</sub>PbI<sub>6</sub>, which is attributable to the localization of CsI due to the columnar growth.

Since it is difficult to uniformly deposit CsI on  $\text{PbI}_2$  films, attempts were made to solve the aggregation of CsI by using co-evaporation, instead of sequential evaporation. Figure 8 shows the cross-sectional SEM and EDX mapping images of co-evaporated  $(\text{CsI})(\text{PbI}_2)/\text{Si}$  stacks before annealing and  $\text{CsPbI}_3/\text{Si}$  stacks after annealing. The coverage of the  $(\text{CsI})(\text{PbI}_2)$  mixed film before annealing is greatly improved compared to the case of sequential evaporation, and no significant element localization is observed. This also affects the structure of the final  $\text{CsPbI}_3$  films after annealing. We see no clear localization of Cs-rich regions even at the tops of the pyramids. We thus consider that by using co-evaporation, the uniform distribution of CsI and continuous film formation are realized. Unlike in the case of sequential evaporation, two kinds of materials are supplied simultaneously on a Si surface in the co-evaporation. The migration of CsI for the uniform composition is thus less significant than in the case of sequential evaporation. Other critical difference between the two evaporation methods is that CsI is deposited on  $\text{PbI}_2$  in the sequential evaporation, while it is evaporated directly on a c-Si surface in the early stage and then on a  $(\text{CsI})(\text{PbI}_2)$  mixed film surface thereafter. This might affect the sticking probability of CsI and the suppression of columnar structures.

We should finally emphasize that the method of conformally forming Perovskite films on textured Si proposed in this study does not include any solution processes at all, unlike in the case of previous studies. This will contribute to the development of industrialized process for Perovskite/Si tandem cells.

#### **4. Conclusions**

The vacuum deposition of  $\text{CsPbI}_3$  films can realize good coverage on pyramidal-shaped c-Si textures, which is difficult by the conventional solution process. The conformal formation of  $\text{CsPbI}_3$  films can be seen independent of pyramid size. The utilization of textured Si leads to a reduction in optical reflectance by  $\sim 10\%$  (absolute), corresponding to an improvement in  $J_{\text{sc}}$  of  $\sim 2 \text{ mA/cm}^2$  in Perovskite/Si tandem cells. Co-evaporation suppresses columnar growth and aggregation of CsI at the top of pyramids. These demonstrate the possibility of realizing new fabrication processes for perovskite/Si tandem solar cells using textured Si.



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## Figure Captions

**Fig. 1.** Surface SEM images of textured Si wafers.

**Fig. 2.** Optical reflectance spectra of textured and flat Si wafers.

**Fig. 3.** Cross-sectional SEM images of a  $\text{PbI}_2$  film formed on textured Si by a solution method.

**Fig. 4.** Cross-sectional SEM images of  $\text{CsPbI}_3$  layers deposited on the three types of textured wafers and on flat Si wafers.

**Fig. 5.** Optical reflectance spectra of three types of textured Si and flat Si coated with  $\text{CsPbI}_3$  layers.

**Fig. 6.** Cross-sectional SEM images of sequentially-evaporated  $\text{CsI/PbI}_2/\text{Si}$  stacks before annealing. (a) and (b) indicate underlying thin layers and columnar structures, respectively.

**Fig. 7.** Cross-sectional SEM and EDX mapping images of sequentially-evaporated  $\text{CsI/PbI}_2/\text{Si}$  stacks before annealing and  $\text{CsPbI}_3/\text{Si}$  stacks after annealing.

**Fig. 8.** Cross-sectional SEM and EDX mapping images of co-evaporated  $(\text{CsI})(\text{PbI}_2)/\text{Si}$  stacks before annealing and  $\text{CsPbI}_3/\text{Si}$  stacks after annealing.

**Table I.** Etching conditions and pyramid heights.

Etchant	SE-2000H		SUN-X 600
Condition	90 °C, 15 min	90 °C, 60 min	70 °C, 50 min
Height ( $\mu\text{m}$ )	2.21 $\pm$ 0.52 [M]	1.47 $\pm$ 0.23 [S]	7.25 $\pm$ 1.68 [L]

**Table II.**  $R_{\text{AVE}}$  of flat and textured c-Si wafers in a wavelength region of 400–800 nm.

	Flat wafer	[S]	[M]	[L]
$R_{\text{AVE}}$ (%)	24.5	11.8	9.9	12.8

**Table III.**  $R_{\text{AVE}}$  on CsPbI<sub>3</sub> layers formed on flat and textured c-Si wafers in a wavelength region of 400–800 nm.

	Flat wafer	S size	M size	L size
$R_{\text{AVE}}$ (%)	16.5	6.6	6.2	6.0

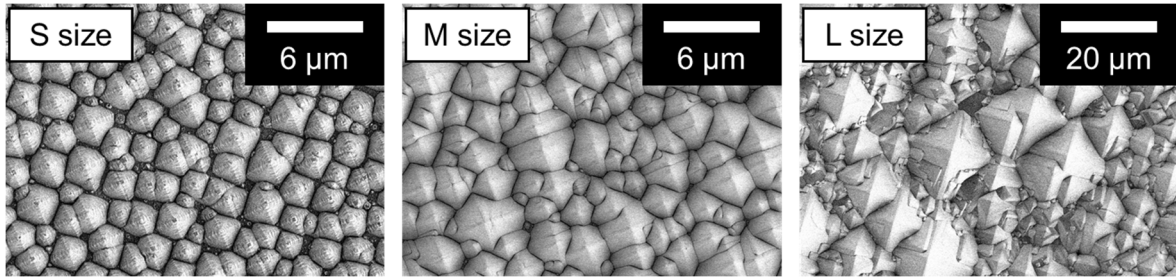


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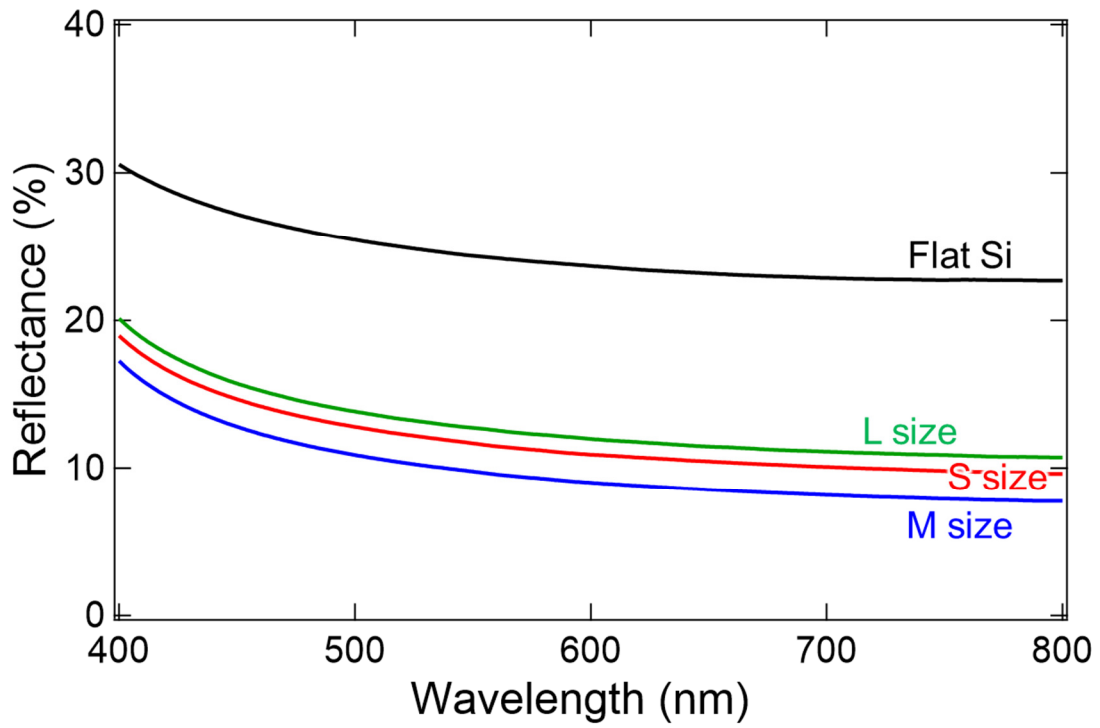


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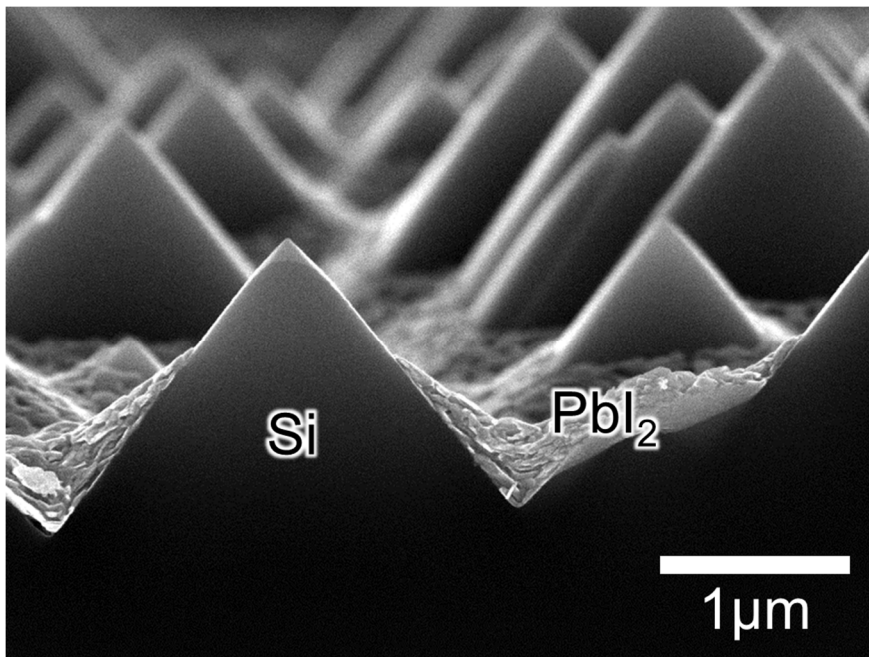


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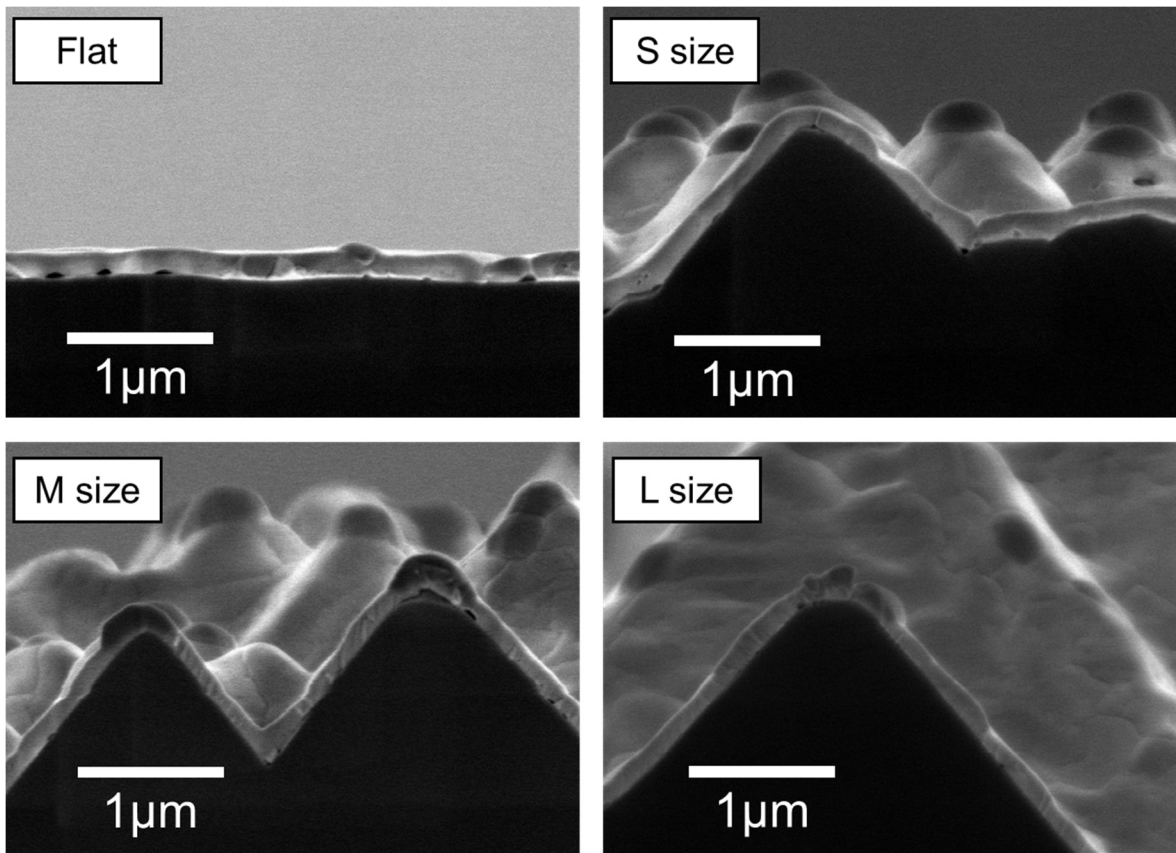


Fig. 4. (Color Online) K. Hamada *et al.*



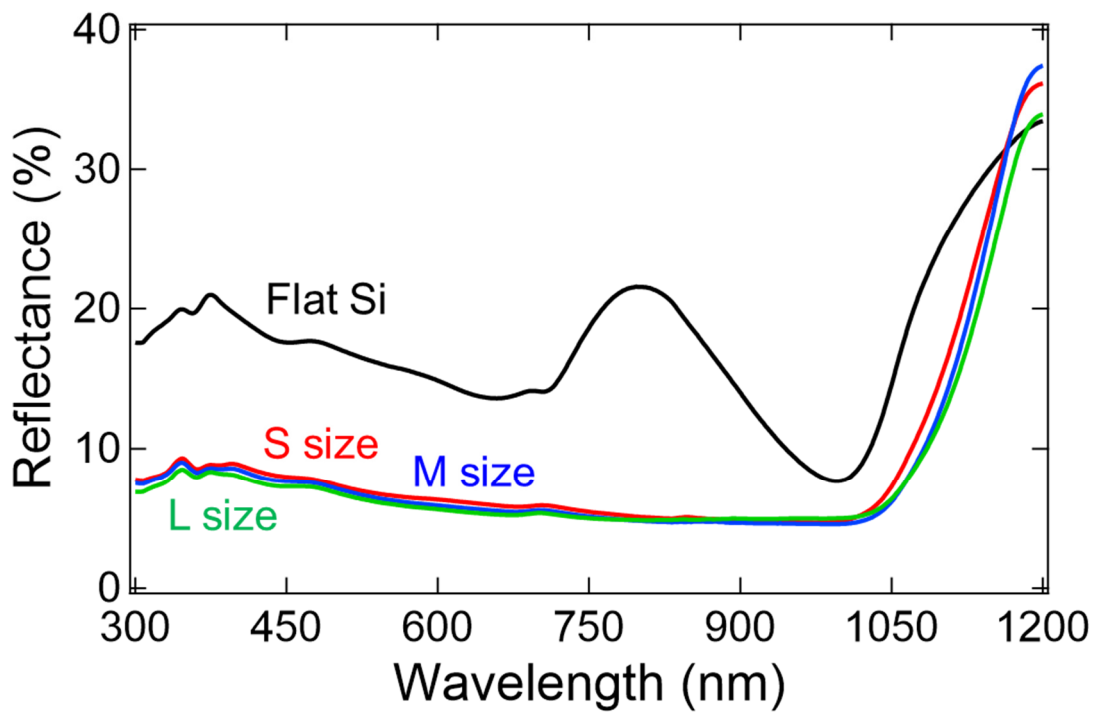


Fig. 5. (Color Online) K. Hamada *et al.*

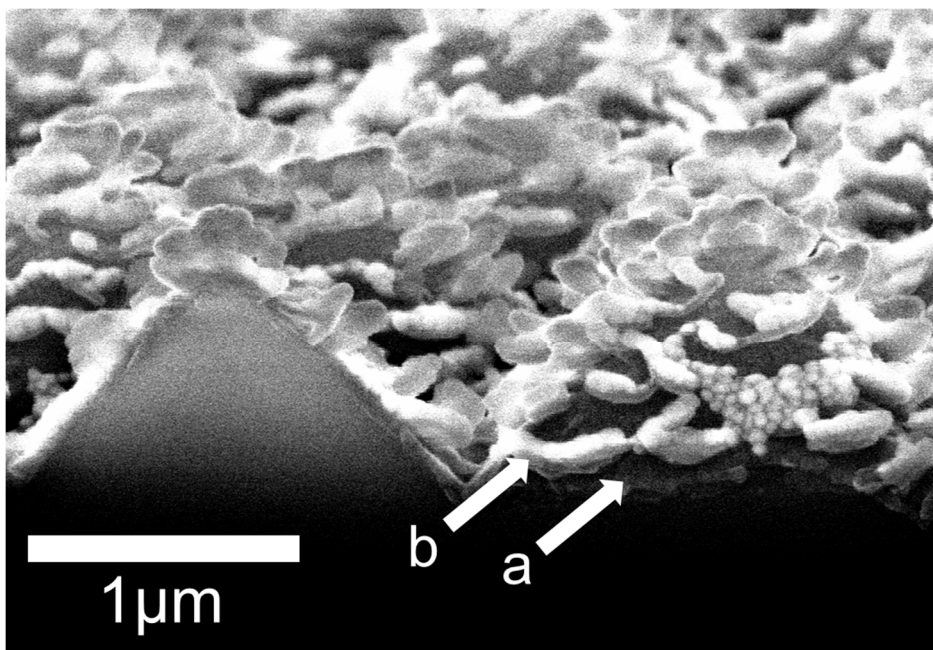


Fig. 6. (Color Online) K. Hamada *et al.*

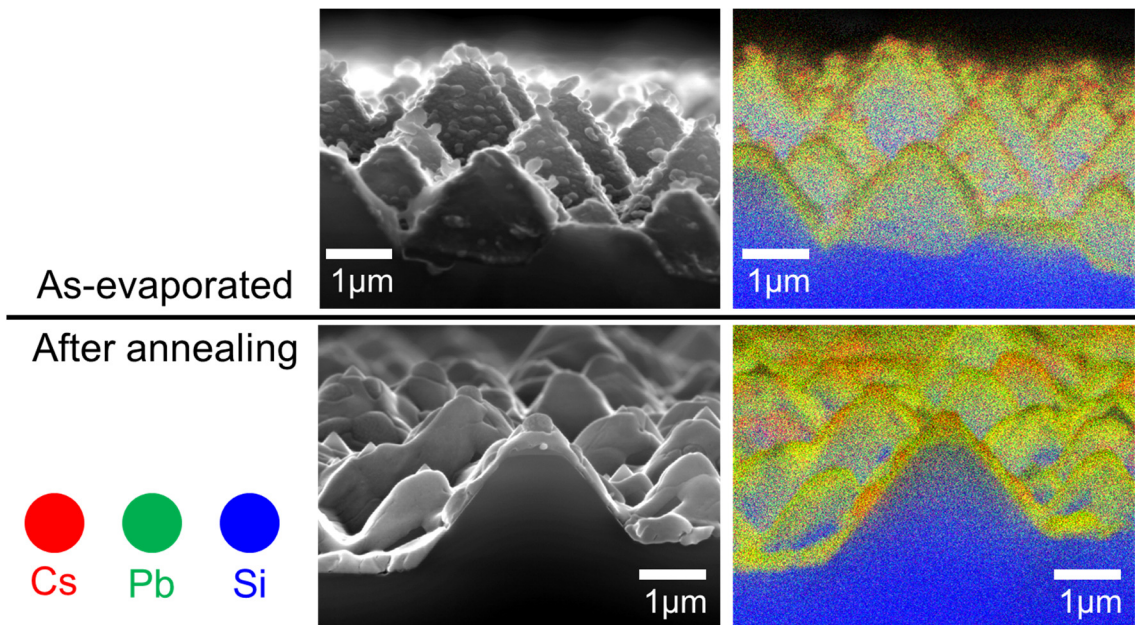


Fig. 7. (Color Online) K. Hamada *et al.*

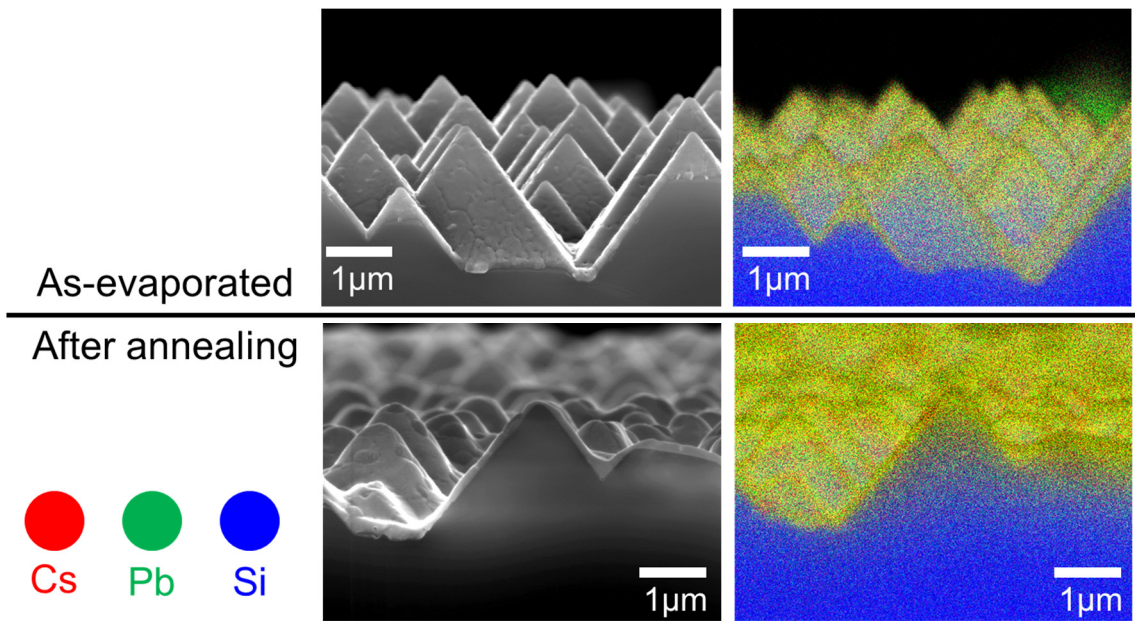


Fig. 8. (Color Online) K. Hamada *et al.*